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FORMATS 2011
Formal Modeling and Analysis of Timed Systems
International Conference
Aalborg, Denmark
21-23 September 2011
http://formats2011.cs.aau.dk
Timing aspects of systems from a variety of computer science domains have been treated independently by different communities. Researchers interested in semantics, verification and performance analysis study models such as timed automata and timed Petri nets, the digital design community focuses on propagation and switching delays, while designers of embedded controllers have to take account of the time taken by controllers to compute their responses after sampling the environment.

Timing-related questions in these separate disciplines do have their particularities. However, there is a growing awareness that there are basic problems that are common to all of them. In particular, all these sub-disciplines treat systems whose behaviour depends upon combinations of logical and temporal constraints; namely, constraints on the temporal distances between occurrences of events.

The aim of FORMATS is to promote the study of fundamental and practical aspects of timed systems, and to bring together researchers from different disciplines that share interests in modeling and analyzing timed systems and resolving temporal constraints (scheduling, worst-case execution time analysis, optimization, model checking, testing, constraint solving).

Typical topics include (but are not limited to):

- **Foundations and Semantics**: Theoretical foundations of timed systems and languages; comparison between different models (timed automata, timed Petri nets, hybrid automata, timed process algebra, max-plus algebra, probabilistic models, type systems).
- **Methods and Tools**: Techniques, algorithms, data structures, and software tools for analyzing timed systems and resolving temporal constraints (scheduling, worst-case execution time analysis, optimization, model checking, testing, constraint solving).
- **Applications**: Simulations, models of computation, systems biology, networked systems, real-time systems, embedded systems, operating systems, economics, and security.

### Invited Speakers
- **Rajeev Alur**
  University of Pennsylvania
  Philadelphia, United States

- **Boudewijn Haverkort**
  University of Twente
  Embedded Systems Institute
  Enschede, Netherlands

- **Oded Maler**
  VERIMAG
  Grenoble, France

### Program Committee
- Christel Baier
- Patricia Bouyer
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- Víctor Braberman
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### Organizing Committee
- MIT-LAB
- Oded Maler
- Boudewijn Haverkort
- Serge Tisserand
- Christian Kratzer